

Fig. 1a

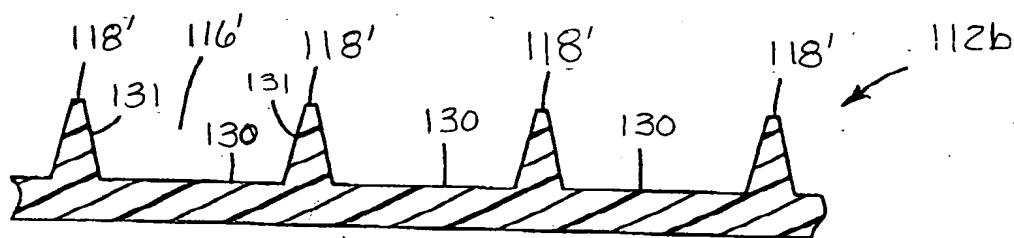


Fig. 1b

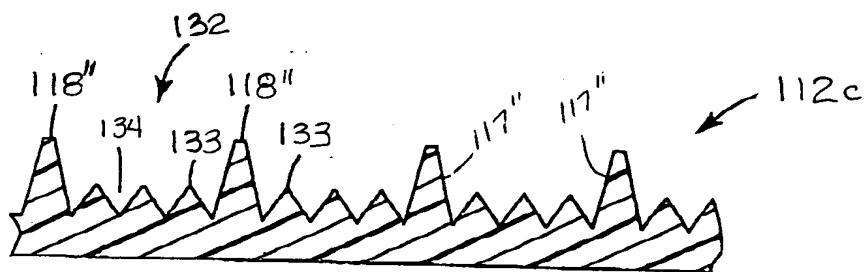


Fig. 1c

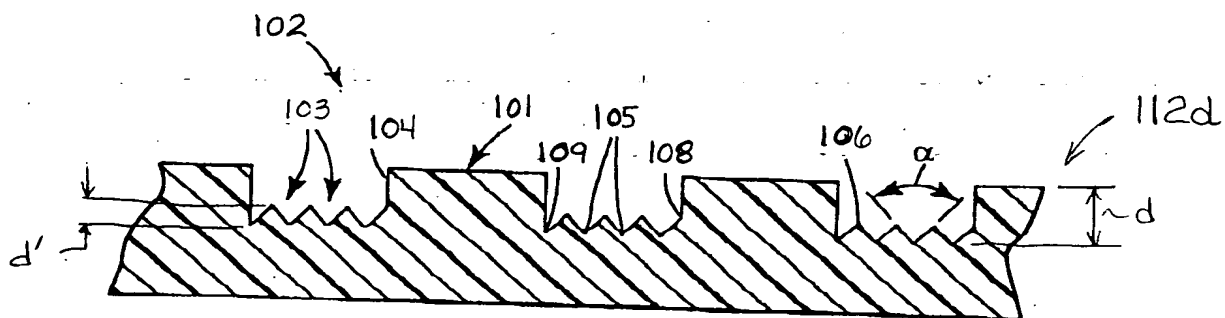


Fig. 1d

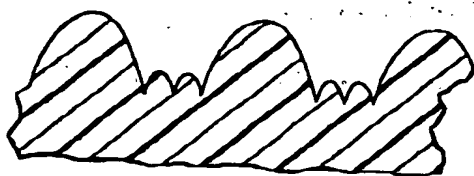


Fig. 19

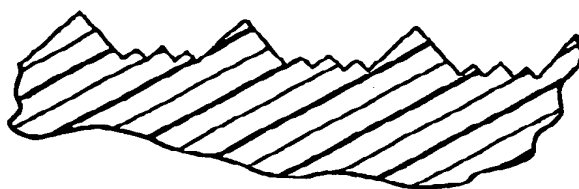


Fig. 1e



Fig. 1f

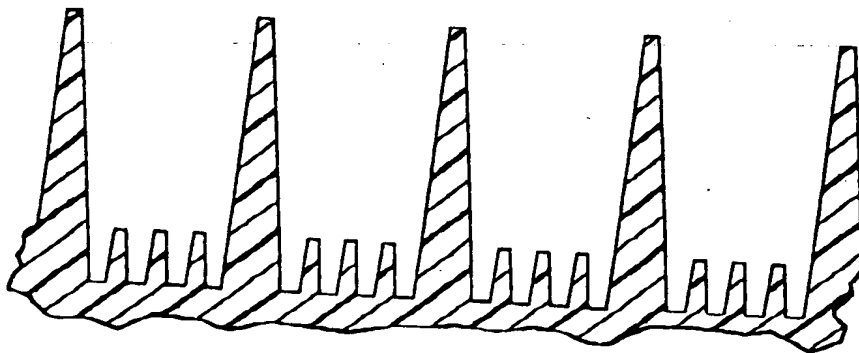


Fig. 1h

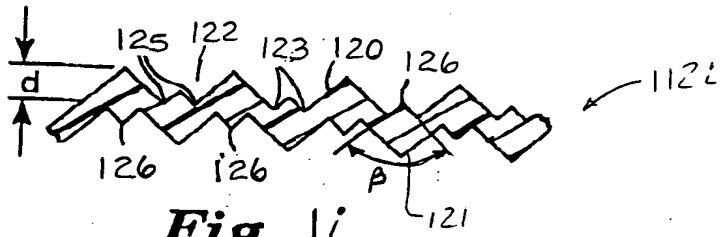


Fig. 1i

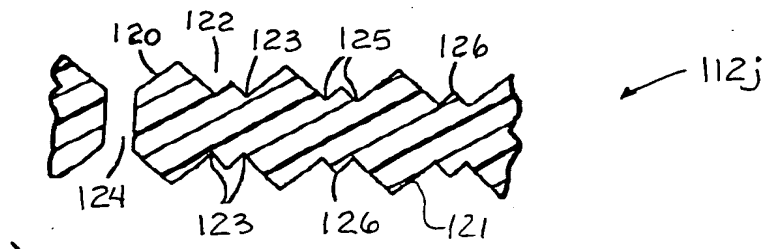
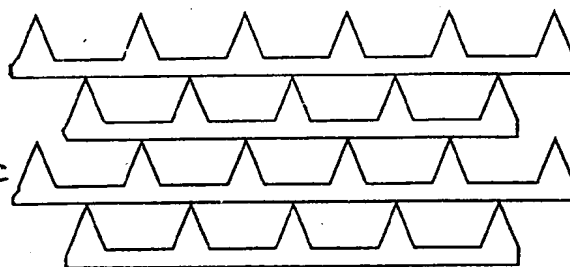


Fig. 1j

FIG. 2c



0961243.83888

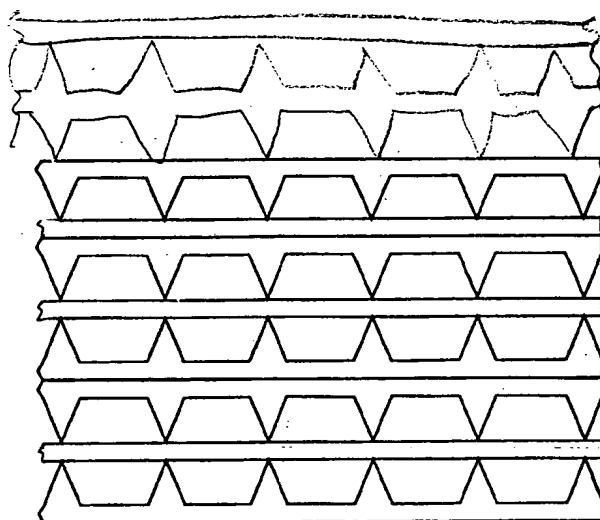


FIG. 2d

Fig. 2f

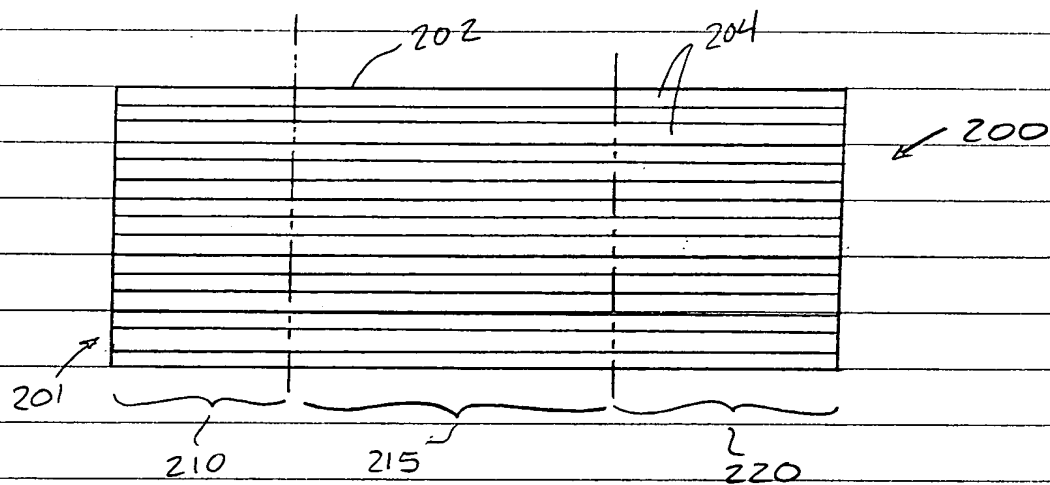


Fig. 4

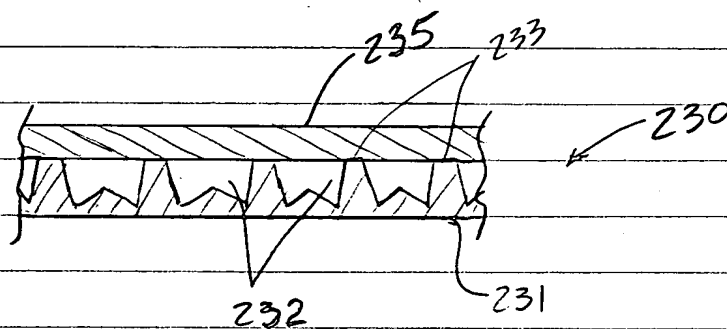


Fig. 5

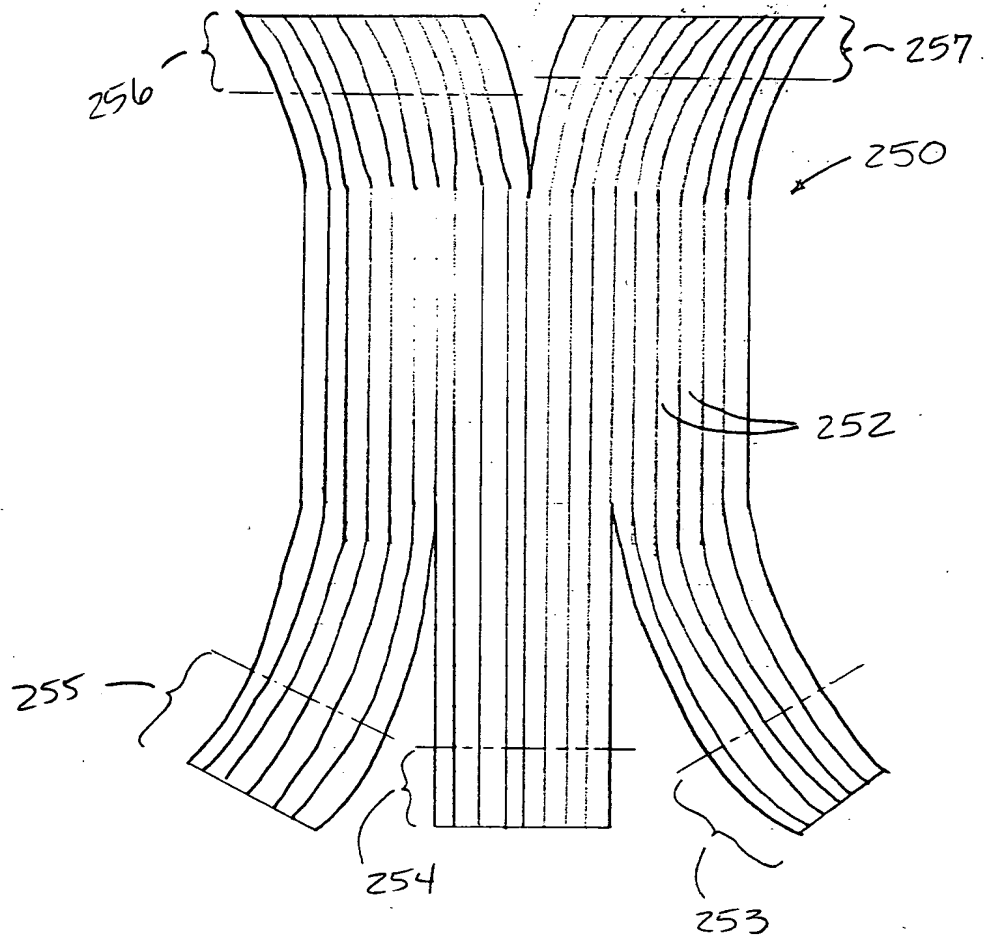


Fig. 7

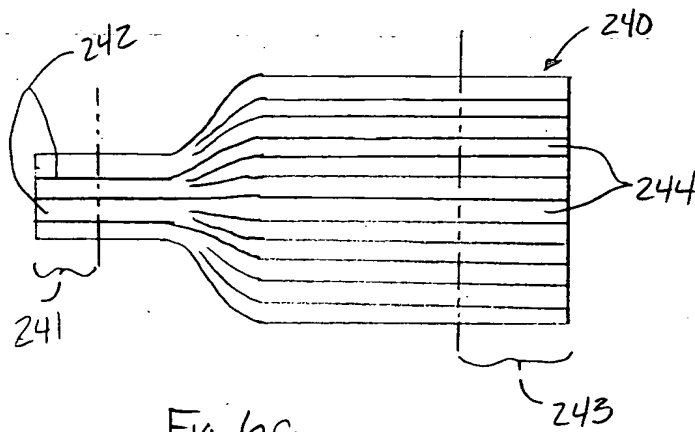


Fig. 6c

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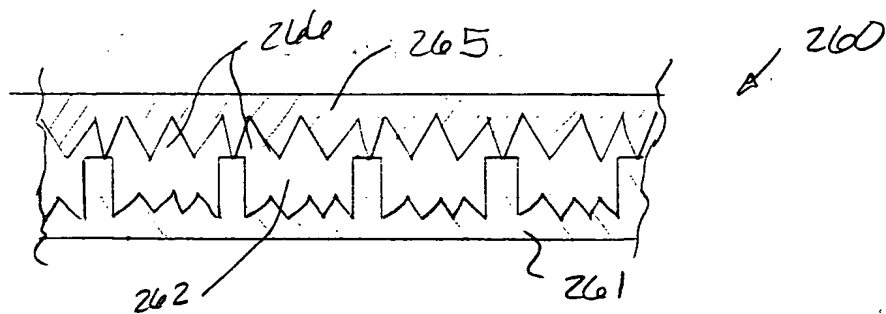


Fig. 8

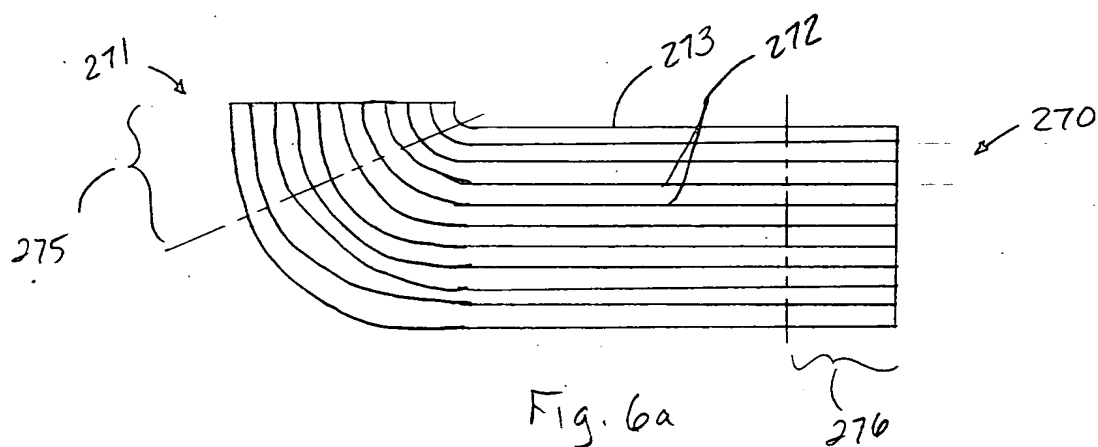


Fig. 6a

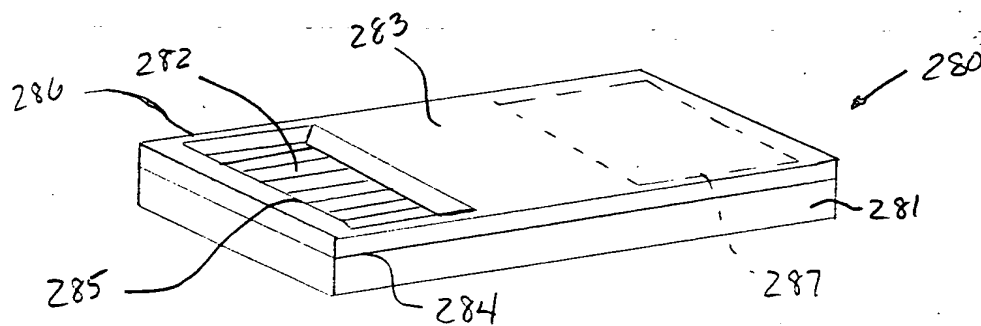


Fig. 6b

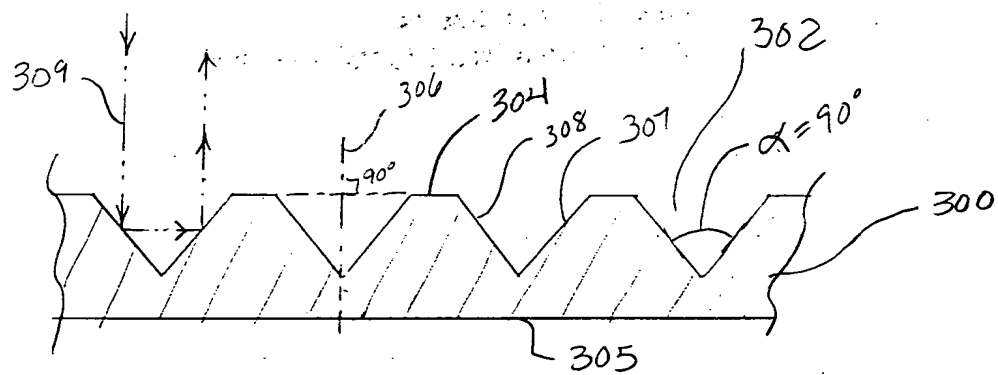


Fig. 9

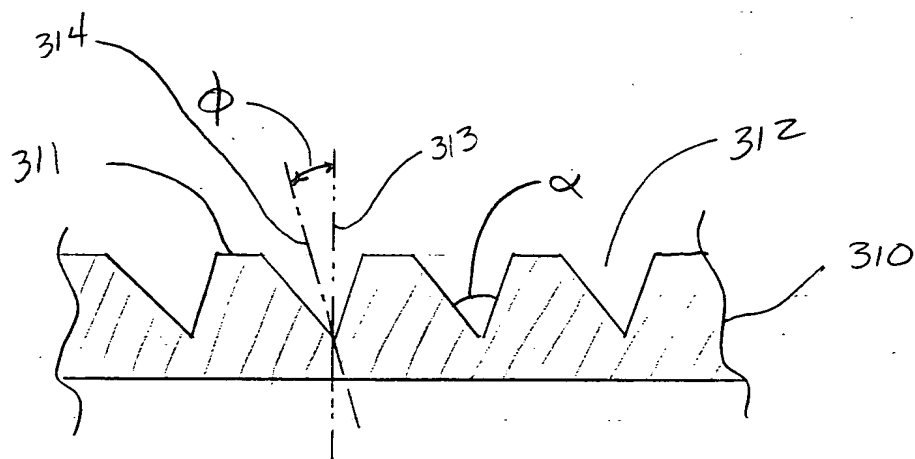


Fig. 10a

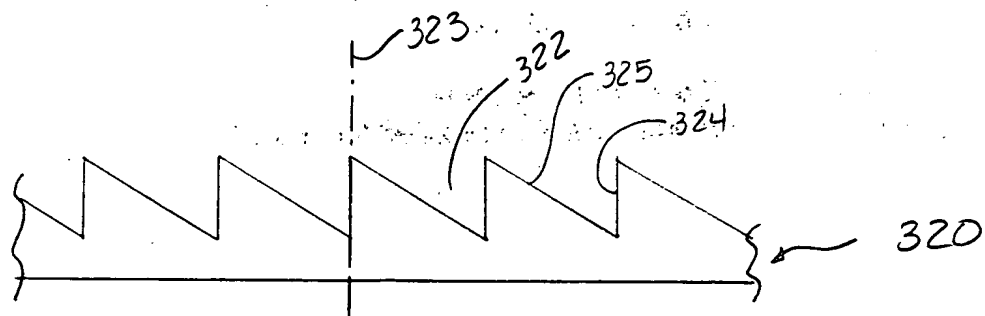


Fig. 10b

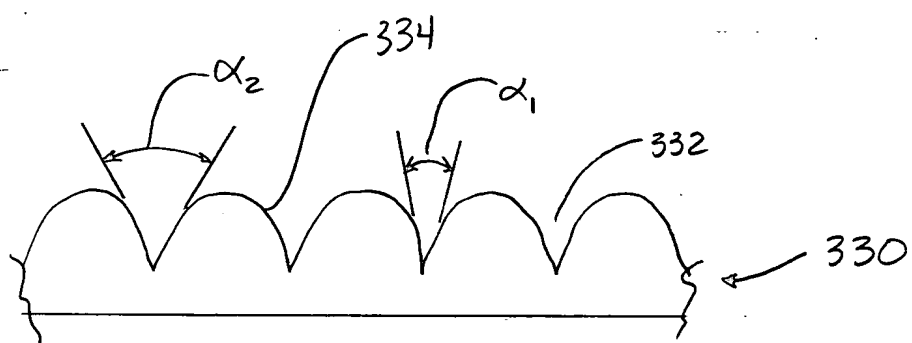


Fig. 10c

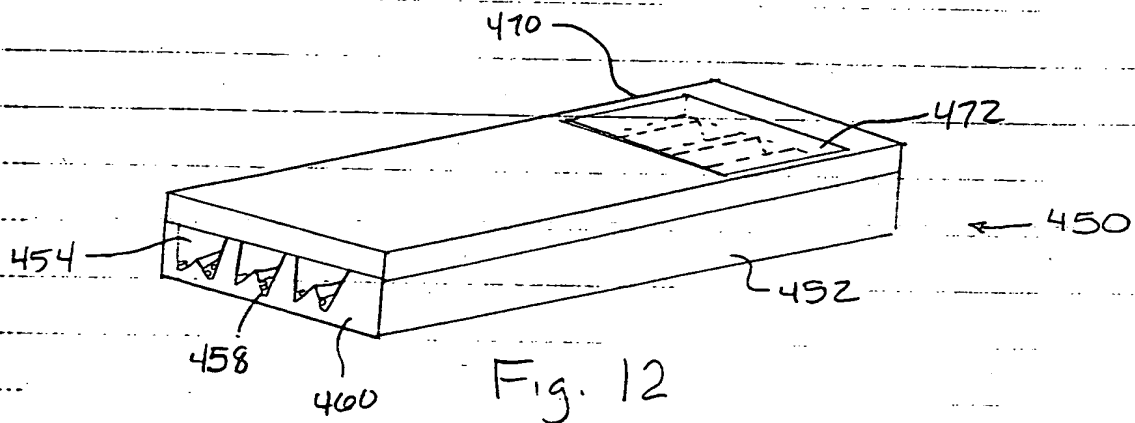
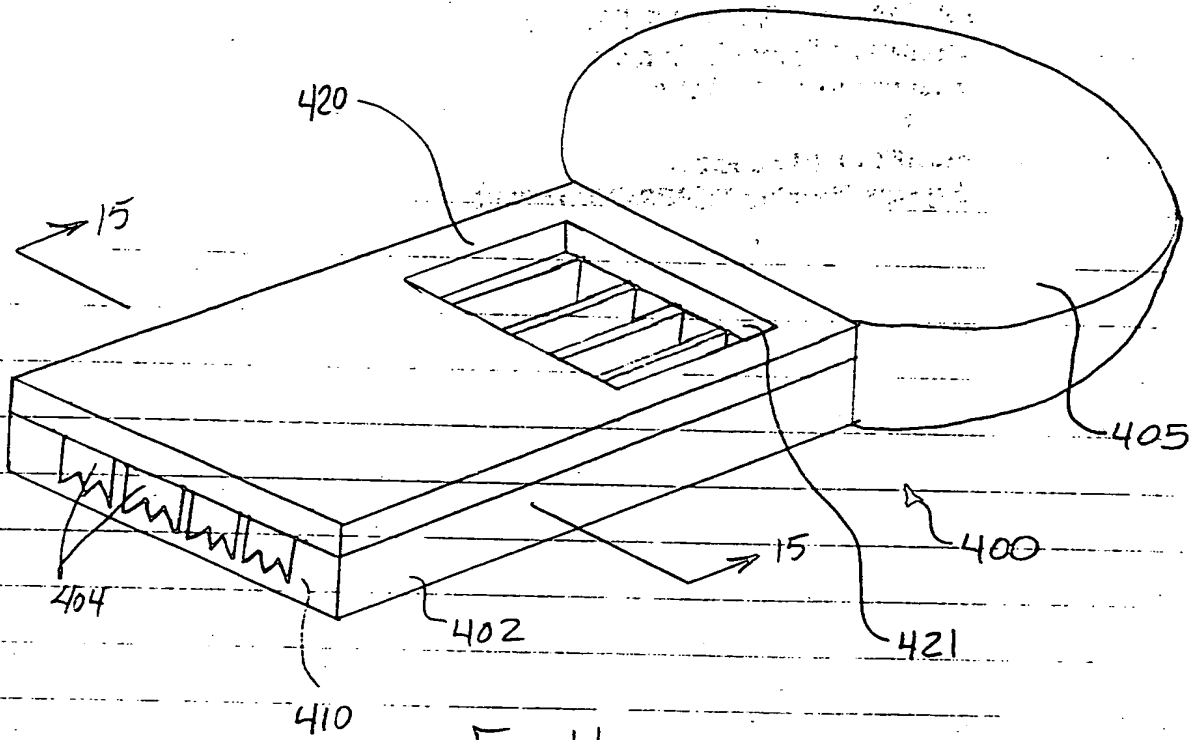
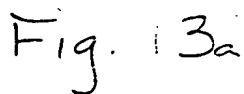


Fig. 13b



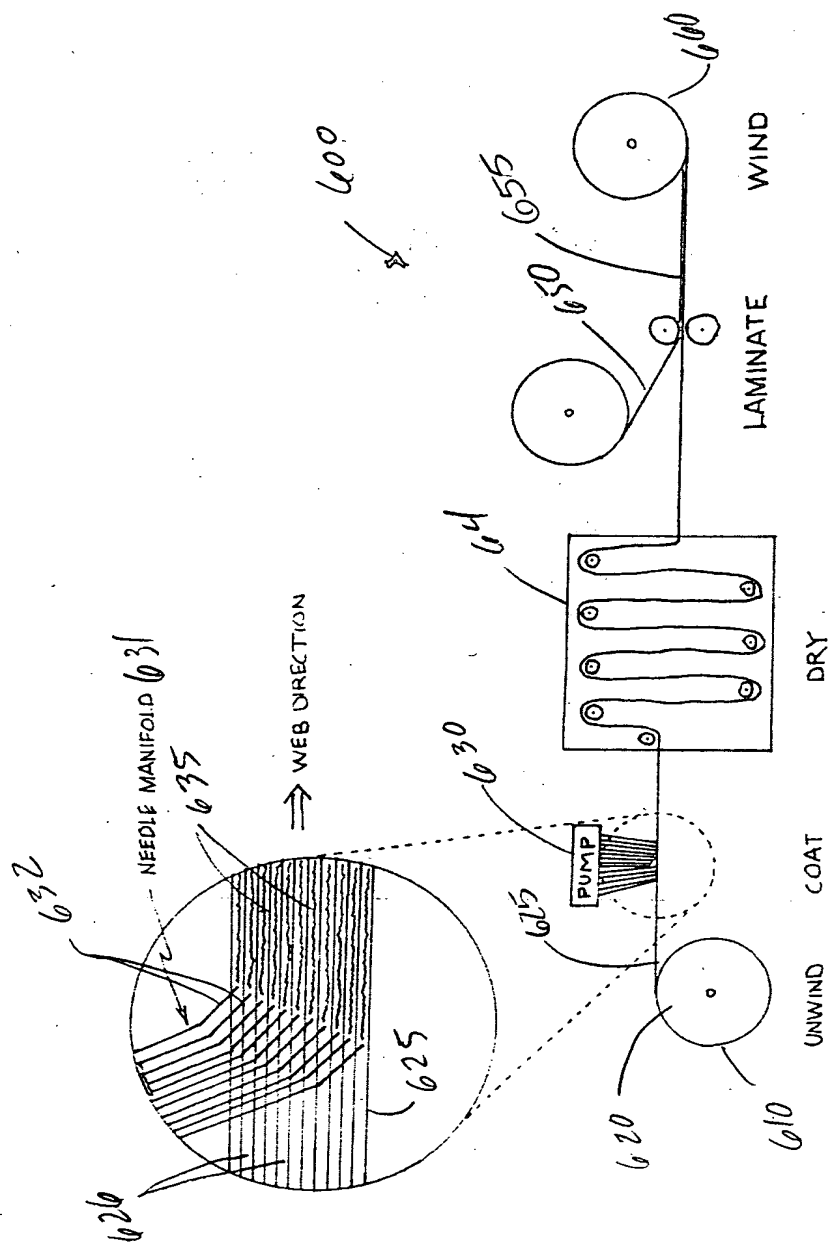


Fig. 14

A cross-sectional view of a device 400. The device consists of a substrate 421, a layer 430, and a series of elements 404. The elements 404 are arranged in a row and are connected to the substrate 421. The layer 430 is positioned above the elements 404. The device 400 is shown in a perspective view.

Fig. 15

Fig. 16

Fig. 17b

